

DIGITAL SPREAD SPECTRUM CIRCUITRY

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ABSTRACT

The frequency of a skew clock signal is dithered around a base frequency, thereby enabling this clock signal to comply with FCC requirements for electromagnetic emissions within a specified window (e.g., a 1 MHz window). That is, delay can be introduced such that the clock signals exhibit slightly different frequencies in successive periods. For example, the frequency of a 100 MHz clock signal can be adjusted to have frequencies of approximately 98, 98.5, 99, 99.5, 100, 100.5, 101, 101.5, and 102 MHz during different periods. This configuration is referred to as a spread-8 configuration, because eight frequencies are used in addition to the base frequency of 100 MHz. Because the frequencies are spread in 0.5 MHz increments, only three of the nine frequencies are included in any 1 MHz window. As a result, 2/3 of the energy of the clock signal is not included when determining whether the clock signal meets the FCC electromagnetic emission requirements in this test. By spreading the frequencies above and below the base frequency in a regular manner, the average frequency of the clock signal becomes equal to the base frequency. Other configurations including, but not limited to, spread-2, spread-4, spread-6, spread-16 and spread-32 configurations, can also be implemented.